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## APPLICANTS

Stefan Scholl, Munich, GERMANY;

\*\* CONTINUING DATA

None NL

\*\* FOREIGN APPLICATIONS

Yes NL

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Verified and Acknowledged Name: <i>Wacker</i> Examiner's Signature: <i>Wacker</i> Initials: <i>NL</i>	GERMANY	4	17	2

## ADDRESS

04743  
 MARSHALL, GERSTEIN & BORUN LLP  
 233 S. WACKER DRIVE, SUITE 6300  
 SEARS TOWER  
 CHICAGO , IL  
 60606

## TITLE

Process for designing and manufacturing semi-conductor memory components, in particular dram components

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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